

Name: Dr.B.Lokeswara Rao

Personal Information



Designation: Professor of ECE & Dean (Academics)

CMR College of Engineering & Technology

Qualification: B.E. (ECE), M.E. (ECE), Ph.D (ECE), FIETE, FIE, MIAE

Experience: 26 years

Specialization: Microwave & Radar Engineering in M.E., Signal processing & VLSI in Ph.D

Date of Joining: 15-09-2014

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Achievements/Publications/Workshops/Seminar/Guest Lectures

Publication

1. Aurdino based Light Monitoring and Control, IOSR Journal of Electronics and Communication Engineering, Dec, 2017
2. Intelligent Power Management System, International Journal of Magazine of Engineering, technology, Management and Research, Dec, 2016
3. Smart Monitoring of Poultry Farm, International Journal of Magazine of Engineering, technology, Management and Research, Dec, 2016
4. Design of PLL Using CS VCO IN 45nm Technology, International Journal of Engineering Research and Management, October, 2016
5. Performance Analysis on Spartan-6, Virtex-6 and Zynq-7000 FPGAs for MicroBlaze Implementation of GPS/INS Integrated Systems, CMR Journal of Engineering & Technology, August, 2016
6. Novel Industrial Wireless Sensor Network for Machine Condition Monitoring, International Journal of Engineering and Science, December, 2015
7. Analysis and Design of Low Power 4-Bit Arithmetic and Logic Unit, International Journal on Computing, Communications and Systems, December, 2015
8. Multi-Dimensional Sensor System for Automotive Crash Prediction Using E-H Field and Ultrasonic Sensor, International Journal of Science, Engineering and Technology, October, 2015
9. Detecting Vital Signs of Chronic Patients with Wireless Sensors Using Bluetooth & Ethernet, International Journal of Research in Advanced Engineering & Technology, September, 2015
10. Design and Testing of Pyramidal Horn, IOSR Journal of Electronics and Communication Engineering, May-June 2015
11. MicroBlaze Implementation of GPS/INS Integrated System on Virtex-6 FPGA, Springer Plus, October 2015
12. MicroBlaze Implementation of GPS/INS Integrated System on Spartan-6 FPGA, Integrated Journal of Engineering & Research Technology, April, 2014
13. MicroBlaze Implementation of GPS/INS Integrated System on Zynq FPGA, International Journal of Electronics and Communication Engineering and Technology, March, 2014
14. Performance Analysis of Micro Blaze Implemented GPS/INS Integrated Systems on Spartan-6 and Zynq-7000 FPGAs, IOSR Journal of Electronics and Communication Engineering, March-April 2014
15. Performance Analysis of Micro Blaze Implemented GPS/INS Integrated Systems on Spartan-6 and Zynq-7000 FPGAs, IOSR Journal of Electronics and Communication Engineering, March-April 2014

16. Stochastic Modelling of MEMS Based Inertial Sensors, International Conference on Sensors, Signal Processing, Communication, Control And Instrumentation, Viswakarma Institute of Technology, January, 2008
17. MEMS Sensors Based Inertial Navigation System, International Conference on Advances in Computing and communication, National Institute of Technology, April, 2011
18. Pre-filtering for MEMS Based Sensors Using Wavelets, International Conference on Signals, System and Automation, GH Patel College of Engineering, January, 2011
19. MEMS Based INS/GPS Integration Using Personal Computer, International Conference on Communication, Computation, Control And Nanotechnology, Rural Engineering College, October, 2010
20. A Review of GPS/INS Integration and Algorithms, International Conference on Emerging Trends in Signal Processing and VLSI Design, Gurunanak Engineering College, June, 2010
21. Modelling Inertial Sensors Errors Using Variance Technique, International Conference on RF And Signal Processing Systems, K L College of Engineering, February, 2008
22. MEMS Based GPS/INS Integration Using Neuro-wavelet, National Conference on Signal Processing, Communications And VLSI Design, Anna University of Technology, May, 2011
23. Filtering of Short-term Noise in Inertial Sensors, National Conference on Emerging Trends in ECE, Panipat Institute of Technology, April, 2011
24. MEMS Based GPS/INS Integration Using Direct Kalman Filter, National Conference on Automation, Communication and Computing, December, 2010
25. MEMS Based INS/GPS Integration Using DSP and FPGA, National Conference on Advances in Communication Technologies, December, 2010

Achievements

1. Published a textbook titled 'Microwave Engineering' in the year 2002.
2. Reviewed the following Text books of the Tata McGraw Hill:
Signals & Systems, Digital Signal Processing, and Digital Image Processing
3. Editorial Board Member-Journal of Wireless Communication Technology, Singapore
4. Editorial Board Member-Journal of Computer Hardware Engineering, USA
5. IUP Journal of Electrical & Electronics Engineering (IUPJEEE), India
6. Received Best Teacher Award from Padmasri Dr. B.V. Raju Institute of Technology, Hyderabad in 2003
7. Fellow of the Institution of Electronics and Telecommunication Engineers, New Delhi
8. Fellow of the Institution of Engineers (India), Kolkata
9. Member of International Association of Engineers, Hong Kong.

Workshops

1. **Convener**- PCB Fabrication & Design, 2016, CMR College of Engineering & Technology, Hyderabad
2. **Convener** - AVR Micro Controllers & Robotics, 2015, CMR College of Engineering & Technology, Hyderabad
3. **Convener**- Analog & Digital IC design-Hands on Experience using Cadence Tools, December 2015, CMR College of Engineering & Technology, Hyderabad
4. **Convener**- SoC System Design & Research Methodology using Xilinx Tools & Techniques October 2015, CMR College of Engineering & Technology, Hyderabad
5. **Convener**- Advanced VLSI Design- Hands on Experience with CAD Tools, December 2014, CMR College of Engineering & Technology, Hyderabad
6. **Coordinator**- National Level Paper & Project Expo, 2014, Bharat Institute of Engineering & Technology, Hyderabad
7. **Chairman**- National Level Work shop, January 2013, Sana Engineering College, Kodad
8. **Chairman**- Faculty Development Program, June 2012, Sana Engineering College, Kodad
9. **Convener**- National Level Symposium, 'Electroblitz-2k11', September 2011, Chaitanya Institute of Science & Technology, Kakinada
10. **Convener**- National Level Workshop on MatLab Applications, March 2011, Chaitanya Institute of Science & Technology, Kakinada
11. **Convener**- National Level Paper & Project Congress, September 2010, Chaitanya Institute of Science & Technology, Kakinada
12. **Coordinator**- National Level Paper & Project Congress, 'Bhaswara-2006', Geethanjali College of Engineering & Technology, Hyderabad.