PERSONAL INFORMATION

Name: Mr.J Nageswara Reddy
Photo:

Qualification:
1. M. Tech (VLSI Systems Design)
2. B. Tech (Electronics & Communications Engineering)

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Designation: assistant Professor, ECE
Courses Taught Previously
UG:
1. Linear & Digital IC Applications
2. Microwave Engineering
3. VLSI Design
4. Switching Theory and Logic Design
5. Digital Design using Verilog
6. Digital Logic Design
7. Signals and System

PG
1. Digital system Design
2. VLSI Design

Various Laboratories Handled:
1. Linear and Digital IC Applications lab
2. Microwave and Digital Communications lab
3. Electronics Device Circuits Lab
4. Electronics circuits and Pulse circuits Lab
5. VLSI Lab

Journals:-
- **Implementation of Binary DAC and Two step ADC Quantizer for CTDS** using gpdk45nm International Journal on Computing, Communications and Systems (IJCCS) ISSN:2277-6699 Vol-4,Issue-2 (December-2015).

- **Design of High speed Standard gate with Subthreshold Dual Mode Logic** using gpd45nm International Journal on Computing, Communications and Systems (IJCCS) ISSN:2277-6699 Vol-4,Issue-2 (December-2015).

- **CMOS RF Process for Wireless Communication with 2.4Ghz fully integrated 1.8V Power Amplifier** International Journal on Computing, Communications and Systems (IJCCS) ISSN:2277-6699 Vol-4,Issue-2 (December-2015).

- **Analysis and Design of Low Power 4-Bit Arithmetic and Logic Unit** International Journal on Computing, Communications and Systems (IJCCS) ISSN:2277-6699 Vol-4,Issue-2 (December-2015).
- International Journal of Recent Advances in Engineering & Technology (IJRAET) **A 350-MS/s Continuous-Time Delta–Sigma Modulator With a Digitally Assisted Binary-DAC and a 5-Bits Two-Step-ADC Quantize in 130-nm** CMOS ISSN (Online): 2347 - 2812, Volume-3, Issue -9, 2015.
- IOSR Journal of Electronics and Communication Engineering **Authenticated Access Control for Vehicle Ignition System by Smart Card and Fingerprint Technology** (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 10, Issue 1, Ver. 1 (Jan - Feb. 2015), PP 45-48.
- International Journal of Engineering Research And Management (IJERM**) Design and Realization of Look-up-Table, FIR Digital Filter** ISSN : 2349- 2058, Volume-01, Issue-08, November 2014.
- Published a journal in Buletin Teknik Elektro dan Informatika on **Subthreshold Dual Mode Logic** Vol.3. No.2. June 2014,pp 141-148,ISSN:2089-3191.
- International Conference on Machine Intelligence Research and Advancement **Low Complexity Out-of-Order Issue Logic Using Static Circuits** 978-0-7695-5013-8/13 $31.00 © 2013 IEEE DOI 10.1109/ICMIRA.2013.67
- **A Neural Network Based Face Detection Using Gabor Filter Response**. International Journal of Neural Networks, ISSN: 2249-2763 & E-ISSN: 2249-2771, Volume 2, Issue 1, pp.-06-09.