

## G. KARTHIK REDDY

### Personal Information

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Designation : Assistant Professor  
Qualification : M.Tech. (VLSI-SD)  
Experience : 10 Years.  
Specialization : VLSI, Design for Testability (DFT)  
Date of Joining: 15/06/2016  
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### Achievements/Publications/Workshops/Seminars/Guest Lectures

#### **Publications:**

- “ Design Low Power Full Adder with Two Transistor EX-OR Gate Using Gate Diffusion Input In 90nm” International Conference on Communications and Cyber-Physical Engineering (ICCCPE – 2018)

#### **Achievements:**

- Participated in four days NSS Special Camp at Bachurajpally (V) from 22<sup>nd</sup> Feb to 25<sup>th</sup> Feb 2018.
- Coordinator (Certificates & Distribution) of SMART INDIA HACKATHON 2018.
- Voluntarily donated the blood in the Blood Donation Camp conducted on 19<sup>th</sup> August 2017.

#### **Workshops:**

- Participated in the three days workshop “IUCEE International Educator Pre-Certification Workshop” from 14<sup>th</sup> May to 16<sup>th</sup> May 2018.
- Participated in the one day workshop on “Digital Design through Aurdino” organised by IIT Hyderabad Teaching Learning Centre on 23<sup>rd</sup> June 2017.