V.Pandu Ranga
Personal Information

Designation: Asst. Professor
Qualification: M.Tech (VLSI)
Experience: 12 Years
Specialization: VLSI System Design
Date of Joining: 18-06-2012
Contact Number: 8328313952
Email Id: vpanduranga@cmrcet.org

SUBJECTS HANDLED:

Electronic Devices & Circuits, Micro Processors & Micro Controllers, Linear and Digital IC Applications, Computer Organization, Digital IC Applications, Electronic Measurements and Instrumentation, Television Engineering, Pulse and Digital Circuits.

Acheivments/Publications/Workshops/Seminars/Guest Lectures

Publications:

1. Published a paper entitled *"RASPBERRY PI BASED INDUSTRIAL PROCESS MONITORING AND CONTROLLING THROUGH WIRELESS COMMUNICATION"* in the **'International Journal Of Innovative Technologies, Volume 5 issue 11 – 2017, ISSN NO:2321-8665'**
2. Published a paper entitled *"WI-FI ENABLED SURVEILLANCE SPYBOT BY RASPBERRY PI"* in the **'International Journal Of Scientific Engineering and Technology Research, Volume 4 , ISSN NO:2319-8885'**
3. Published a paper entitled *"IMPLEMENTATION OF A WIRELESS SENSOR NETWORK PLATFORM FOR MONITORING TEMPERATURE IN A FOREST AREA "* in the **'International Journal of Scientific Engineering and Technology Research, Volume 03 issue 48 December-2014' IJSETR, ISSN NO:2319-8885**.
4. Published a paper entitled *"MEASURING OF AUTOMOBILE PARAMETERS AND ITS SPEED CONTROL"* in the **'International Journal of Scientific Engineering and Technology Research' Volume 03 issue 50, December-2014, IJSETR** Journal.
5. Published a paper entitled *"VEHICLE DATA ACQUISITION AND MONITORING SYSTEM USING CAN"* in the **''International Journal of Scientific Engineering and Technology Research' Volume 04 issue 35, August-2015, IJSETR** Journal.
6. Published a paper entitled *"A GREEN PATH FOR AMBULANCE"* in **'IJMETMR Journal, ISSN NO:2348-4845'**.

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<u>Workshops / FDP's Attended:</u>

1. Attended and participated in a two day intensive course (20th and 21st July-2007) on *"Microwave Measurements"* conducted by Dept of ECE, CEME University College of Engineering, Osmania University.

2. Participated in one week Faculty Development Program on *"Challenges in FPGA Based Digital System Design"* conducted by ECE Department Sri Vishnu Engineering College For Women Bhimavaram from 6th to 18th June 2011.

3. Participated in three day Workshop on "**Rough Sets Approach For Engineering Applications**" organized by Department of ECE MRCET, Hyderabad from December 2-4, 2013

4. Participated in one week Faculty Development Program on *"Advanced VLSI Design Hands On Experience With CAD Tools"* conducted by ECE Department, CMRCET, Hyderabad from 19th to 24th December 2014.

5. Participated in two day Faculty Development Program on *"SOC System Design And Research Methodology Using XILINX Tools & Techniques"* conducted by ECE Department CMRCET, Hyderabad on 8th & 9th October 2015.

6. Participated in one week Faculty Development Program on *"Analog And Digital IC Design Hands On Experience Using Cadence Tools"* conducted by ECE at CMRCET, Hyderabad from 26th November to 1st December 2015.

7. Participated in Faculty Development Program on *"Research Proposals – Funding Agencies, Opportunities And Methodologies"* conducted by CMRCET, Hyderabad on 14th November 2015.

8. Participated in Faculty Development Program on *"Digital Design Through Arduino"* conducted by IIT Hyderabad at CMRCET on 23rd June 2017.

9. Participated in one week Faculty Development Program on *"Advances In Microwave Antenna Design"* organized by the E & ICT Academy, NIT Warangal at K.L University, Vijayawada from 8th to 13th May 2017.

10. Participated in one week Faculty Development Program on *"Real Time Embedded Systems & IOT and Its Applications "* organized by the E & ICT Academy , NIT Warangal at IARE Dundigal, from 27th November to 2nd December 2017.

11. Participated in one week Faculty Development Program on *"Recent Trends in IC Design And Its Applications "* organized by the E & ICT Academy , NIT Warangal at SITE Tadepalligudem , from 16th to 21st April 2018.

12. Participated in two week Faculty Development Program on *"Signals Transform Techniques and their Applications "* organized by the E & ICT Academy , NIT Warangal at CMRCET, Hyderabad from 21st May to 30th May 2018.