## **Personal information**

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Current Designation Assist Professor

PG-Qualification M.Tech VLSI SYSTEM DESIGN

UG B.TECH-ECE

Experience: Teaching:6 Years

Industrial: Nil Total :6 Years

Linear and Digital Integrated Circuits, Cellular Mobile Communication, Satellite Communication,

Electro Magnetic Interference and Electro

Magnetic Compatibility,

Subjects Thought: Electronic Measurements and Instrumentation,

TSSN.

Labs Handles IC HDL Lab, EPDC Lab, MWE Lab, DC Lab.

Achievements Oualified Gate -2018.

## Work shop and Quality Up-gradation Programs attended /participated:

- ➤ Participated in the Two Weeks Faculty Development Programme (FDP) on "Signal & Transform Techniques and their Applications", 2018 ,from ECE Department of CMRCET,In Association with E & ICT Academy, NIT Warangal,
- ➤ Participated in the One Week Faculty Development Programme (FDP) on "Advances in wireless communication systems", 2017, conducted by department of ECE, CMRCET, Hyderabad.
- ➤ Participated in Faculty Development program on "Effective teaching methodology", 2012, Organized by Noble group of institutions, Junagadh, Gujarat.
- ➤ Participated in Faculty Development program on "Teaching- Learning- Methodologies", 2008, organized by Sarada institute of technology and science, Khammam.

## **Journals (National / International):**

- 1. RESCUE BOT FOR BORE WELL ACCIDENTS, IJRAT, April, 2017.
- 2. Traffic signal control system in emergency by using solar and wind energy, IJESC, April, 2017.