Mrs. CHOWTAPALLI NEELIMA

Personal Information

Designation	: Assistant Professor
Qualification	: M.Tech.
Experience	: 8 years
Specialization	: VLSI SYSTEM DESIGN
Date of Joining	: 05-12-2015
Phone Numbers	: 7702536062
Emai-Id	: chneelima@cmrcet.org
Subjects taught	: EMTL, AWP, EC, STLD, AC DC, LICA and TSSN
Labs Handled	: BS Lab, DSP Lab, AC&DC Lab MPMC and MWE Lab

Achievements/Publications/Workshops/Seminar/Guest Lectures

Publications

1. Ravinder Tirupati, ch Neelima,"*FPGA Implementation of VLSI Architecture for Data Compression",* proceedings of IJEICE-2013, Nov 22, 2013, India, PP. 2105- 2114, published in **IJERT**.

2. Ravinder Tirupati, CH Neelima, A Deepthi,"*Design and Implementation of Wi-FiMedium Access Control Layerfor Transmitter with VHDL",* proceedings of IJSR- 2013, December 2013, India, PP. 123-128, published in **IJSR**.

Achievements

1. Qualified UGC NET for Assistant professor.

2. Qualified JNTU FET for Assistant professor.

Workshops / FDP's Attended:

1. **One-Week Faculty Development Program on Antenna wave propagation in** the year of 2017, in association with E & ICT Academy, NIT Warangal.
2. **One-week Faculty Development Program on Adaptive Signal Processing** from 4th-9th June 2018, in association with E & ICT Academy, NIT Warangal.