

Rajidi Sahithi
Personal Information



Designation : Assistant Professor
Qualification : M.Tech (VLSI System Design)
Experience : 8 Years 7 Months
Specialization : Wireless Communications and VLSI
Date of Joining : 10/07/2017
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Achievements/Publications/Workshops/Seminars/Guest Lectures

Publications:

1. Presented a Paper on "An Evaluation of ICI Reduction Techniques in OFDM System" in **International Journal of Computer Applications -IJCA**, 2017 ISSN: 0975-8887
2. Presented a Paper on "Implementation of Area Efficient OFDM Architecture" in **International Conference of Signal Processing Computing and Communication-System Design (ICSPCOMSD)**- volume 2 August-2016, ISBN: 978-93-83038-42-8.
3. Published a Paper on "Implementation Area Efficient Low Power OFDM Architecture" in **International Journal of Advanced Research in Computer and Communication Engineering (IJARCCE)**- volume 4 issue 7 November 2015, ISSN 2278-1021
4. Presented a Paper on "Optimizing the Modified Booth Recorder for Fused Add-Multiply Operator" in **SSRG International Journal of VLSI & Signal Processing (SSRG-IJVSP)** – volume 2 Issue 2 March-April 2015, ISSN: 2394 - 2584
5. Presented a Paper on "The Development of Embedded and Embedded Linux Application in Intelligent Monitoring and Controlling System" in **International Journal on Computing and Communications (IJCCS-14)**, ISSN:2277-6699
6. Presented a Paper on "The Development of Embedded and Embedded Linux Application in Intelligent Monitoring and Controlling System" in **International Conference on Communications, Signal Processing Computing and Information Technology (ICCSPCIT-14)**, Third Edition-2014 , Pages : 163-167 ISBN:978 93 83038 27 5

FDPs and Short Term Courses:

1. **Two-Weeks Faculty Faculty Development Programme on Signals & Transform Techniques and their Applications** from 21st-30th May 2018, In association with E & ICT Academy, NIT Warangal.
2. **A 8 week course on Estimation For Wireless Communications: MIMO-OFDM Cellular And Sensor Networks** from July-September 2017 by IIT Kanpur (NPTEL Course).
3. **One week Faculty Development Programme on Advances in Wireless Communication Systems** from 11th-15th December 2017. (CMRCET).
4. **A Two week ISTE STTP on CMOS, Mixed Signal and Radio frequency VLSI Design** Conducted by **IIT Kharagpur** from 30th January to 4th February 2017 in MRCET.
5. **One week FDP on Application Oriented Teaching Methodologies** from 15th to 19th November 2016 (MRECW).
6. One day Workshop on **Research Trends in VLSI & Embedded Systems** on 5th November 2016 under TEQIP-II (JNTU, HYDERABAD).
7. **One week FDP on Effective Teaching Methodologies for Engineering Education** from 1st to 5th November 2016 (MRECW).
8. **A One week Refresher Course on VLSI & EMBEDDED SYSTEMS** from 22nd to 28th June 2016 (MRECW).
9. **Two Day Workshop on VLSI DESIGN USING MENTOR GRAPHICS** from 15th to 16th June 2016(Core EL & Digilent Inc, USA in MRCET).
10. Workshop on **EMBEDDED LINUX ON ZYNQ-7000 SOC USING VIVADO** on 25th September 2015. (Core EL & Digilent Inc, USA in MRCET).

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11. One Week Refresher course on **DIGITAL SIGNAL PROCESSING** (MRECW).
12. Two Week ISTE-STTP Main on **INTRODUCTION TO DESIGN OF ALGORITHMS** Conducted by **IIT Kharagpur** from 25th to 30th May 2015 in MRCET.
13. A two day FDP on **VLSI Design Using Cadence Tools** from 27th to 28th June 2014 (MRECW).
14. A ten day FDP on **VLSI Design Flow using Cadence tool** (Geethanjali College of Engineering and Technology, Cheeryal ,HYD).
15. FDP on **TWO DAY HANDS ON PROGRAM ON ANALOG SYSTEM DESIGN** from December 12 to 13th 2012 (Anurag College of Engineering, Ghatkesar).
16. FDP on **VLSI Design Flow using Synopsys EDA tool** from 11th to 12th February 2013 (Seer Akademi USA Inc., Kukatpally, HYD).
17. FDP on **ARM Cortex M3 Architecture** from 2nd to 3rd July 2010. (SNIST, Ghatkesar, HYD).
18. **A Three Day Faculty Development Programme** from 22nd to 24th June 2011 (Geethanjali College of Engineering and Technology, Cheeryal ,HYD).
19. **VLSI DESIGN METHODOLOGIES AND TESTING PRINCIPLES** from August 24th to 4th September 2009. (Vardhaman College of Engineering, Shamshabad).