

M.SIRIN KUMARI

Personal Information



Designation: Assistant Professor

Qualification: M.Tech

Experience: 10 YEARS

Specialization: VLSI SYSTEM DESIGN

Date of joining: 05/07/2017

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Achievements/Publications/Workshops/Seminar/Guest Lectures:

Award and Fellowship:

- Certified from Wipro Mission 10X for high Impact teaching skills

Other Responsibilities:

- Program Coordinator of Electronics and Communication Engineering Dept
- Managing Academic Projects

Work shop and Quality Up-gradation Programs attended /participated:

- Participated in the Two Weeks Faculty Development Programme (FDP) on “Signal & Transform Techniques and their Applications”, 2018 ,from ECE Department of CMRCET, In Association with E & ICT Academy, NIT Warangal,
- Participated in the Refresher course on “VSLI Design and Embedded systems”, 2007 From JNTUH Sponsored by UGC
- Participated in the MHRD sponsored Advanced technology programme on “VLSI SYSTEM DESIGN”, 2008 conducted by department of ECE, National Institute of Technology, Warangal, Ap
- Participated in Faculty Development program on “ANALOG & DIGITAL CMOS IC DESIGN FLOW USING MENTOR GRAPHICS EDA TOOLS”, 2012 Organised by Mall Reddy Institute Of Technology And Science, Secunderabad

- Participated in the TWO DAY NATIONAL WORKSHOP on "MATLAB & ITS APPLICATIONS", March 2012 Organized by Dept of ECE, Conducted by Sree Chaitanya Institute Of Technological Sciences Karimnagar
- Mission 10X workshop on High Impact Teaching Skills, June 2011, conducted at Sree Chaitanya College of Engineering, Karimnagar.

Paper published in International refereed Journal/Conference:

- International Conference on "ADVANCED COMPUTING, COMMUNICATION AND NETWORKS" June 2011 (ICACCN'11) Sponsored by International neural network society (INNS), organized by Universal Association Of computer and Electronics Engineers at Chandigarh
- A National conference on "RECENT ADVANCEMENT IN COMMUNICATIONS & ELECTRONICS (RACE'11)" Organized by Department of ECE KITS, Singapur, KNR