

Dr.D.Vijayakumar

Personal Information



Designation: Associate Professor

Qualification: M.E.,Ph.D.,

Experience: 15 years

Specialization: Embedded System Technologies

Date of Joining: 15-02-2018

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Achievements/Publications/workshops/Seminars/Guest Lectures

Publications:

1. D.Vijayakumar, V.Malathi “A Real-Time Management and Evolutionary Optimization Scheme for a Secure and Flexible Smart Grid towards Sustainable Energy”, International Journal of Electrical Power and Energy Systems, **ELSEVIER. Publications**, June 2014, Vol.62, pp.540-548, ISSN 0142-0615.
2. D.Vijayakumar V.Malathi “An Enhanced ACO and PSO based Fault Identification and Rectification Approaches for Power Transmission Systems”, International Transactions On Electrical Energy Systems, January 2017,ISSN 2050-7038, Wiley-Blackwell Publications .
3. D.Vijayakumar, “Parallel Processing in Embedded Systems Using Multidimensional Retiming”, International Journal of Advanced Engineering Technology / Vol.II / Issue II/April-June, 2011/239-244.
4. D.Vijayakumar, V.Malathi, “A Flexible Scheme for Transmission Line Fault Identification Using Image Processing For a Secured Smart Network”, International Journal of Electrical & Electronics Engg. Vol. 2, Spl. Issue 1 (2015) ,ISSN: 1694-2310,pp: 1694-2426

International and National Conferences

- Presented paper Titled, “A Flexible Scheme for Transmission Line Fault Identification Using Image processing for a Secured Smart Network”, Presented in International Conference on Electronic Design Innovations and Technologies at NITTTR, Chandigarh, on April 2015.
- Presented a paper on ‘Enhancement of Smart grid Performance through Logic based Fault Tolerant MPSoC’ in the 5th Joint International Conference on Advances in Electrical Measurements and Instrumentation Engineering, Cochin on May 2016, and published in Tata Mc Graw Hill Publications.
- Presented paper Titled, “Embedded Based Multiprocessor Architecture for Real Time Management and Restructuring of Flexible Smart Grid”, Presented in International Conference on ICCECON 2k16 at CKC Institute, Ernakulam during March 2016
- Presented paper Titled “Parallel Processing In Embedded System Using Multidimensional Retiming” in National Conference On Embedded Systems -2007 Organized by IEEE Student Branch in Arulmigu Kalasalingam College of Engineering during March 2007
- Presented paper Titled, “Analysis of communication Infrastructure for the smart grid”, in National Conference on Innovations in engineering and Technology at Anna university of Technology Madurai, during April 2012.
- Presented paper Titled “Parallel Processing In Embedded System Using Multidimensional Retiming” in Second National Conference On Embedded System and VLSI Design in Adhiyaman College of Engineering , during March 2007.
- Presented paper Titled, “Parallel Processing In Embedded System Using Multidimensional Retiming “, in National Conference on Emerging Trends and Technologies in Arulmigu Kalasalingam College of Engineering during March 2007.
- Presented paper Titled, “VLSI Implementation of OFDM-CDMA systems to network on chip”, in National Conference on Extreme engineering and technological advancements 2k8 at Jayam college of engineering, during April 2008.

Faculty Development Programme, Workshop and seminars

- Participated in one day faculty development programme on MATLAB organized by the Institution of Engineers (India), Madurai local center on June 2013.

- Participated in three days faculty development programme on Modern teaching methods in engineering and technology organized by Latha Mathavan Engineering college, on June 2011.
- Participated in one day faculty development programme on Smart grid organized by the Institution of Engineers (India), Madurai local center on December 2013.
- Participated in two days faculty development programme on, “Research Issues in Electrical Engineering”, organized by Velammal college of Engineering and Technology, Madurai on March 2011.
- Participated in one day faculty development programme on, “Programming skills using MATLAB”, organized by Latha Mathavan Engineering college, on April 2014.
- Participated in one day workshop on, “Research Methodology”, organized by Centre for Faculty development, Anna University, Chennai, on December 2014.
- Participated in one day workshop on, “VLSI Design”, organized by Thiagarajar College of Engineering, on February 2009.
- Participated in one day workshop on, “Research Issues in Embedded system and Digital signal processing Using FPGA”, organized by Anna University of Technology, Madurai, on May 2011.
- Participated in one day workshop on, “Research Methodology: Objectives and Approaches”, organized by Anna University of Technology, Madurai, on December 2010.
- Participated in two days workshop on, “VLSI system Design”, organized by National Engineering College, on September 2007.
- Participated in one day workshop on, “Recent trends in Modern analytical techniques and energy storage devices”, organized by Latha Mathavan Engineering College, on November 2009.
- Participated in one day workshop on, “Recent Trends in Nano technology”, organized by Latha Mathavan Engineering College, on October 2009.
- Participated in two days National seminar on, “Micro sensors development and deployment challenges”, organized by Sethu Institute of Technology, on December 2007.
- Participated in National Symposium “FACTS” held in December 2005 conducted by EEE Department in Arulmigu Kalasalingam College of Engineering.

- Participated in the Workshop on “Embedded System in Power Electronics” held in August 2006 conducted by EEE Department in Arulmigu Kalasalingam College of Engineering and technology.