B.Chakradhar Personal Information



Designation: Asst. Professor Qualification: M.Tech (DSCE)

Experience: 8 Years

Specialization: Signal Processing & Speech processing.

Date of Joining: 18-06-2012 Contact Number: 9985626596 Email Id: bchakradhar@cmrcet.org

SUBJECTS HANDLED:

Signals and systems, Digital signal processing, Electronic Devices & Circuits, Micro Processors & micro Controllers, Control systems, Computer organization, Digital IC Applications, VLSI Design.

Acheivments/Publications/Workshops/Seminars/Guest Lectures

Publications:

- 1. Published a paper entitled "DESIGN OF PUBLIC TRANSPORT SERVICE SYSTEM BASED ON SMS" in 'IJERA Journal, Volume-3, Issue 1, Jan-Feb 2013'.
- 2. Published a paper entitled "BLUETOOTH BASED HOME AUTOMATION SYSTEM" in the 'International Journal Of Engineering Trends & Technology, Volume 4 issue 9 2013'
- 3. Attended the Two Day workshop on "Mentoring Skills and thinking skills for Engineering Faculty" held during 18-19, July 2014 organized under TEQIP II at JNTU Hyderabad.
- 4. Published a paper entitled "FINGER VEIN BASED SECURED AUTHENTICATION SYSTEM" in the 'International Journal of Advanced Engineering Research and Science, Volume 1 issue 6 November-2014'.
- 5. Published a paper entitled "FINGER VEIN PATTERN BASED AUTHENTICATION (SOME INVESTIGATIONS)" in the 'International Journal of Advanced Engineering Research and Applications' special Issue/ presented in the 'National Conference on Developments, Advances & Trends in Engineering Sciences" organized by CMREC Hyderabad.
- 6. Published a paper entitled "HOME AUTOMATION SYSTEM THROUGH WIRELESS SENSOR NETWORK" in the 'International Journal of Advanced Engineering Research and Applications' special Issue/ presented in the 'National Conference on Developments, Advances & Trends in Engineering Sciences" organized by CMREC Hyderabad.

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- 7. Published a paper entitled "MULTITASKING REAL-TIME SCHEDULING IN A ROBOT" in the International Journal of Engineering & Computer Science, Volume 04 Issue 10 October-2015.
- 8. Published a paper entitled "COGNITIVE RADIO COMMUNICATION USING RASBERRY PI" in the International Journal of Engineering Science and Computing, Volume 06 Issue 10 October-2016.

Workshops / FDP's Attended:

- 1. Successfully completed the "*JKC training*" at JPNCE from December-2007 to April-2008.
- 2. Attended and participated in a three day short term course(12th to 14th Feb-2007) on "*Mathematical Perspectives in Signals & Systems*" conducted by Dept of ECE, JPNCE in association with RGMCET, SNIST and Signal & Pattern Engg. Society of India.
- 3. Stood first in college in post graduation and received gold medal.
- 4. Participated in the three day State Level Workshop on "Basic Simulation: Multi-Scheme and Multi Platform Approach" conducted by ECE Department (JPNCE) from 7th to 9th July 2010.
- 5. Participated in "*MATH PER DSP*" an event conducted on digital signal processing in Jaya Prakash Narayan College of Engineering.
- 6. Participated in the "National Convention for Academics & Research" organized by free software movement of India and hosted by Swecha at Hyderabad from December 16-18, 2010.
- 7. Participated in the Two Day Workshop on "*Advanced VLSI Design*" during 31st August to 1st September, 2012 at CMRIT Hyderabad.
- 8. Attended one week FDP on "Signals & Systems" during 5th June to 10th June 2017 organized by Geethanjali College of Engineering & Technology.
- 9. Attended one week FDP on "New Algorithms for Signal Processing Hands On" during 20th April to 27th April 2018 organized by LBRCE, Mylavaram in association with E & ICT Academy, NIT Warangal.
- 10. Attended "One week intensive training on Lab View" at Vasavi college of engineering organized by NI.
- 11. Attended two weeks FDP on "Signals & Transform Techniques and their Applications" during 21st 30th May 2018 organized by CMRCET, Hyderabad in association with E & ICT Academy, NIT Warangal.