Name: Dr. G. Venkata Hari Prasad,
Designation: Professor -ECE;
Qualification: B.E (ECE); M.Tech (ECE- DE&CS); Ph.D (ECE-BMSP); MIETE; MISTE; MIE
Experience: 18 years
Specialization: Digital Electronics & Communication Systems - M.Tech.,
              Signal processing - Ph.D.,
Date of Joining: 24-06-2019
Phone Number: 9885071439
Email-Id: drgvenkatahariprasad@cmrcet.org

CONFERENCES/JOURNALS

- **Dr. G. V. Hari Prasad,** R. D. Bhavani, "Improved Classification of Phonocardiography Signal Using Optimised Feature Selection" Indian Journal of Science and Technology(**Ind.JST**)**,** *September 2017*

- V. Sirisha Rani, **Dr G. V. Hari Prasad** (2017) "Automatic Segmentation and Detection Of Heart Sound Components S1 S2 S3 S4 Using Pseudo-Wigner Ville Distribution, Hilbert Vibration Decomposition" International Journal of Advance Research and Latest Trends

- **Dr. G. V. Hari Prasad**, S. Kireeti (2017), "Wavelet Feature Selection Approach for Heart murmur Classification" Published through NCITDCE-17

- P.Priyanka, **Dr. G. V. Hari Prasad** (2017), "Detection of VT,VF Sounds Using SVM & Zero Crossing Rate" Published through NCITDCE-17

- **Dr. G. V. Hari Prasad**, S. Kireeti (2017), "ECG Noise Reduction By Transient Suppresion Through Genetic Algorithm &Wavelet Filtering" International Journal of Advance Research and Latest Trends

- P. Shiny Angel, **Dr. G. V. Hari Prasad** (2017), "Detection of Abnormal Beats Using Zero Crossing Information" International Journal of Advance Research and Latest Trends

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar (2016) "Automatic Abnormal murmur detection in Phonocardiography Signal Using Hybrid Feature Selection" International Conference on Emerging Technologies, Networking and Computational Intelligence- 16(ICETNCI-16), March 2016.

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar "Wavelet Feature Selection Approach for Heart murmur Classification" World Academy of Science, Engineering & Technology **(WASET)**, 2015.

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar "Performance Analysis of Different feature Selection methods for feature extracted Phonocardiography (PCG) Signals" **INCEMIC-2015**" Organized by Andhra University College of Engineering(A), AU, Visakhapatnam, Jul 2015.

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar "Genetics for Enhanced Heart Murmur Classification" International Journal of Applied Engineering Research **(IJAER),** ISSN 0973-4562 (2015)

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar " Analysis of various DWT methods for feature Extracted PCG Signals" International Journal of Engineering Research& Technology **(IJERT)**, ISSN 2278-0181 (2015)

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar "Of Feature Selection & Classification algorithms for Phonocardiography (PCG) Signal" International Journal of Applied Engineering Research **(IJAER)**, ISSN 0973-4562 (2014)

- **G.V. Hari Prasad,** E. Rama Krishna reddy, "**Content based image retrieval using support vector machine in digital image processing techniques"** an International Journal of Engineering Science & Technology**(IJEST)** , ISSN: 0975-5462 ,2012

- **G.V. Hari Prasad,** Dr P.Rajesh Kumar, V.Srinivasa rao, K. Leelabhavani "**Simulation, Analysis and Comparison of SET and CMOS hybrid circuits using SPICE Model"** an International Conference on MEMS & Optoelectronics Technologies**(ICMOT-10)** in Collaboration with **International Accreditation Council of Quality Education & Research(IACQER)**, On Jan 22nd -23rd SCET, Narsapuram.

- V.Srinivasa rao, Dr P.Rajesh Kumar, **G.V. Hari Prasad"Discrete cosine transform Vs Discrete wavelet transform: An objective comparison of image compression techniques for JPEG Encoder"** at International Journal of Advanced Engineering & Applications (**STEPS 2010) on Jan 2010**, A STEPS Education Initiative, India

- V.Srinivasa Rao, **G.V. Hari Prasad**, Dr P.Rajesh Kumar **"JPEG Encoder using SPARTAN-3E FPGAs"** at International Conference Systemics, Cybernetics and Informatics (**ICSCI 2010) on Jan 2010**, Under the aegis of Pentagram Research Centre, India.

- Participated in the "**5th International Conference on Trends in Industrial Measurements and automation**" (TIMA-2007) at NIT, Tiruchirappalli from 4th -6th Jan2007.

- **G. Venkata Hari Prasad** & Dr. P. Rajesh Kumar " Study on PCG Signals Using MATLAB and LABVIEW" DST Sponsored National Conference on Advances in MEMS,VLSI & Instrumentation **(NCAMVI-2015)**" Organized by LBRCE. pp. 30-38.

- V.Srinivasa Rao, **G.V. Hari Prasad"Floating point FPGA Vs Conventional FPGAs: An objective comparison of FPGA resources for modeling performance"** at Fourth National Conference signals, systems and security **(NCSSS-2010) on FEB 26-27, 2010**, Bannari Amman Institute of Technology, Tamil Nadu.

- K. Leelabhavani, **G.V. Hari Prasad**, **"Low Power BS LFSR using multi Scan chain"** a 3rd National Conference on VLSI, Embedded Systems, Signal processing and Communication Technology**(NCVESCOM' 2010) on 2nd &3^{rd}April 2010** at Aarupadai Institute of Technology,Vinayaka Missions University, Tamilnadu.

- V.Srinivasa rao, Dr P.Rajesh Kumar, **G.V. Hari Prasa** "**Discrete cosine transform Vs Discrete wavelet transform: An objective comparison of image compression techniques for JPEG Encoder"** at Fourth National Conference on signals, systems and security **(NCSSS-2010) on FEB 26-27, 2010**, Bannari Amman Institute of Technology, TamilNadu.

- Dr P.Rajesh Kumar ,V.Srinivasa rao, **G.V. Hari Prasad** " **Hardware / Software co-design of an efficient JPEG Encoder using low cost FPGAs"** at National Conference on Advances in signal processing **(NCASP-09)**, Andhra university, Visakhapatnam.

WORK SHOPS/FDP's

- One day **Pre- Conference Tutorial on** "**Embedded Systems on Chip**" at NIT, Tiruchchirappalli on 4th Jan 2007.

- Three day national level **"Workshop on Microwave Electronics WOME -2007"** at SVECW Bhimavaram.

- Two day National workshop on **"FPGA based System Design"** at Sir C.R.R College of Engg- Eluru from 28th -29th Sep 2007.

- Three day workshop on **"Advances in Signal Processing and Communication Systems"** at KITS-Warangal.

- One day workshop on "**Low Power VLSI Design and Technology**" at Andhra University, Visakhapatnam.

- Two day workshop in collaboration with **CoreEL Technologies on "Digital Design Simplified-Taking Design from Simulation to Layout Extraction"** at SVECW Bhimavaram.

- Two day Work shop on "PCB Design" at URCET, Telaprolu from 25th - 26th Sep 2012.

- Two day Work shop on "3D Technology & Applications" at URCET, Telaprolu from 28th - 29th June 2013.

- Two day **"National workshop on Advances in Signal Processing (NWASP-13)"** from 30th-31st Aug 2013 at Andhra University, Visakhapatnam.

- AICTE Sponsored Three day National workshop on **"Signal and Image Processing using LABVIEW (SIPLV-2013)"** on 11th -13th Nov 2013 at RVR&JC CE, Guntur.

- Two day National workshop on **"High Speed VLSI Circuit Design (HSCD-2014)"** on 7th -8th Feb 2014 at GITAM University, Visakhapatnam.

- Faculty Development Programme (FDP) on **"Hands on Training Program on MATLAB and its Applications in Digital Image Processing"** on 20th -21st Mar 2014 at LBRCE, Mylavaram.

- AICTE Sponsored Two day National workshop on **"Atmosphere Weather Radars and GPS (AWRGPS-2014): Recent Developments and Future Challenges"** on 28th -29th Mar 2014 at LBRCE, Mylavaram.

- One week Faculty Development Programme (FDP) on **"Recent Trends in Systems and Signal Processing (RTSSP- 2014)"** on 22nd -27th Dec 2014 at LBRCE, Mylavaram.

- Faculty Development Programme (FDP) on **"Applications of MATLAB in Engineering"** on 11th -12th Dec 2015 at ALIET, Vijayawada.

- Convener- NBA Orientation Program, June 2016, ALIET, Vijayawada

- Convener- National Conference "NCITDCE-17", April 2017, ALIET, Vijayawada

TRAINING PROGRAMS

- Six day Faculty Orientation Programme on "**Expectation, Inspiration, Aspiration"** at Loyola-ICAM College of Engineering & Technology, Chennai held during 20th - 25th June 2016.

- Five day Faculty Training Programme on "**RF Circuit Design using ADS Software: Hands on Experience"** under TEQUIP-II, Organized by ECE,SVECW in collaboration with Agilent Technologies held during 17th -21st June 2015 at Shri Vishnu Engineering College for Women, Bhimavaram.

- Attended a week AICTE Short Term Tanning Programme on "**Signal, Image and Speech processing**" at NITK, Surathkal.

- Attended a one week TEQUIP Short Term Tanning Programme on "**Analog IC Design**" at NIT Warangal.

- Two Day "**Student counseling and career guidance**" at SVECW, Bhimavaram by NITTTR, Chennai from 20th -21st June 2008.

- Six Day "**Quality Improvement Programme on Instructional Design and Delivery**" at Sir C. R. Reddy College of Engg., Eluru by NITTTR, Chennai from 12th -17th June 2006.

RESOURCE PERSON

- Acted as a Resource Person in the one week Faculty Development Programme (FDP) on **"Recent Trends in Systems and Signal Processing (RTSSP-2014)"** for the session **Biomedical Signal Processing** on 24th Dec 2014 at LBRCE, Mylavaram.

- Acted as a Resource Person in one day Workshop on **"Advanced Algorithms in Digital Signal Processing"** for the session **Biomedical Signal Processing & its Applications** on 19th March 2016 at ALIET, Vijayawada.

GUEST FACULTY

- Acted as a Guest Faculty for the session **Microcontrollers and its applications** on 30th Apr 2015 at GDMM College of Engineering for women, Nandigama.

- Acted as a Guest Faculty for the session **Advanced DSP methods and its applications** on 30th Jul 2017 at Sunflower College of Engineering, Lankapalli

Achievements

- GATE-2001, 82 Percentile
- University 3rd Rank in M.Tech
- R**atified as an Assistant Professor** one time in JNTUH (2007) and two times in JNTUK (2010, 2014)
- **Ratified as an Associate Professor** one time in JNTUK (2015) and one time in JNTUH (2018)
- **Ratified as PRINCIPAL** in JNTUH (2017)
- Appointed as a Chief marker; first time introduced online valuation at JNTU Kakinada in May 2013 for Analog & Digital IC Design.
- Acted as a Judge for "**IETE STUDENTS DAY: A Student technical Paper Contest-2k17**", Vijayawada Local centre & ALIET conducted in ALIET- ECE, Vijayawada on Feb 01st 2017.

- Member of Indian Society for Technical Education, New Delhi
- Member of the Institution of Electronics and Telecommunication Engineers, New Delhi
- Member of the Institution of Engineers (India), Kolkata