



Prof E.N.V.Purna Chandra Rao, Head of the Department has completed AMIE with Electronics and Communication Engineering from The Institution of Engineers(India),Calcutta, M.Tech with Digital Systems and Computer Electronics from College of Engineering,Jawaharlal Nehru Technical University, Anantapur,Andhra Pradesh, Pursuing his PhD from JNTUH,Hyderabad.

He has vast experience of 19 years of industry experience which includes 15 years Indian Air Force comprises of various levels of technical experience along with administrative roles. He worked in Radar stations and Missile stations. He obtained Aircraft maintenance BAMEL licences in Electrical and in Radar Systems. He has 14 years of teaching experience in the JNTU affiliated engineering colleges and taught almost all subjects of ECE branch.

#### **Papers & Conferences:-**

- 1) Ninety-Six Phase Code Design with Good Discrimination Factor S.P Singh<sup>1</sup>, **E.N.V. Purna Chandra Rao**<sup>2</sup>, S Babu Rao<sup>3</sup> T D Bhatt<sup>4</sup>, 29th Convention of Electronics & Telecommunication Engineers, October 29-30, 2013, Hyderabad
- 2) Twenty Phase Code Design for Radar E.N.V.Purna Chandra Rao,S.P.Signh, International Journal of Advanced Computer Research(ISSN(print):2249-7277 ISSN(Online):2277-7979) Volume-3 Number-4 Issue-13 December-2013.
- 3) Orthogonal Sixty Four Phase Sequence Sets Design Using MGA, E.N.V.Purna Chandra Rao,Dr S.P.Signh,Dr T.D.Bhatt,Proceedings of 4<sup>th</sup> International Conference on Innovations in Electronics & Communication Engineering(ICIECE-2015)
- 4) **Binary Codes Sidelobes Reduction Techniques** E.N.V. Purna Chandrar, Dr S.P Singh<sup>2</sup> Dr T D Bhatt<sup>3</sup> 1ECE Department, GNITS, Hyderabad, India 2&3 ECE Dept, MGIT, Gandipet, Hyderabad, India E-mail : [singh\\_spgahlot@rediffmail.com](mailto:singh_spgahlot@rediffmail.com) 10th International Radar Symposium India - 2015 (IRSI - 15).
- 5) One Eighty Phase Code Design with Good Discrimination Factor E.N.V. Purna Chandra Rao<sup>1</sup> and S.P Singh<sup>2</sup>1 ECE Department, GNI, Hyderabad, India 2ECE Department, MGIT, Hyderabad, India [singh\\_spgahlot@rediffmail.com](mailto:singh_spgahlot@rediffmail.com) Proceedings of International Conference on Innovations in Electronics and Communication Engineering (ICIECE-2016)

- 6) Three Sixty Phase Code Design with Good Discrimination Factor **E.N.V. Purna Chandra Rao<sup>1</sup>, S.P Singh<sup>2</sup>, T D Bhatt<sup>3</sup>** <sup>1</sup> ECE Department, CMRCET, Hyderabad, India <sup>2&3</sup> ECE Department, MGIT, Hyderabad, India Conference Proceeding of 4th International Conference on New Frontiers of Engineering, Science, Management and Humanities (ICNFESMH-2017) at The Institution of Electronics and Telecommunication Engineers, Osmania University Campus, Hyderabad, India on 3rd September 2017, ISBN: 978-81-934288-3-21275
  
- 7) Three Sixty Phase Code Design with Good Discrimination Factor E.N.V. Purna Chandra Rao<sup>1</sup>, S.P Singh<sup>2</sup>, T D Bhatt<sup>3</sup> <sup>1</sup> ECE Department, CMRCET, Hyderabad, India <sup>2&3</sup> ECE Department, MGIT, Hyderabad, India: International Journal of Electronics, Electrical and Computational System IJEECS ISSN 2348-117X Volume 6, Issue 9 September 2017.