

B.VENKATESHWAR RAO

Personal Information



Designation: Assistant Professor

Qualification: M.Tech

Experience: 16 years

Specialization: Digital Systems And Computer Electronics

Date of Joining: 6/24/2019

Phone Numbers: 8985193337

Emai-Id: bvrao.be@gmail.com

Achievements/Publications/Workshops/Seminar/Guest Lectures

Publication

1. “Multiband slotted Elliptical Printed Antenna Design and Analysis” in JOURNAL OF MECHANICS OF CONTINUA AND MATHEMATICAL SCIENCES on July-August (2019) pp 378-386 ISSN (Print) 0973-8975.
2. “Effect of 2D EBG Structure on Monopole Antenna Radiation and Analysis of its Characteristics” in GLOBAL CONFERENCE ON COMMUNICATION TECHNOLOGIES-2015 (IEEE) on 23rd and 24th April 2015 ISSN/ISBN No. 978-1-4799-8553-1/15
3. “Sensors and Signal Conditioners” in an International Journal of Innovation in Electronics and Communication (IJIEC) ISSN: 2249-9946, Volume 4, Issue 1 June-2014
4. “A Novel Approach for Design and Development of L-Band Patch Antenna” in an International Journal of Computing Technologies (JCT) ISS:2278-3814, Volume 2, Issue 8 AUGUST-2013
5. “A modern approach for design and optimization of helical Antenna used at transmitting end of GPS” in an International Journal of Emerging Trends & Technology in Computer Science (IJETTCS) ISSN 2278- 6856, Volume 2, Issue 3, May – June 2013

6. “Consumer Contactless Transactions Using ARM9 Processor” in an International Journal of Computer Science & Informatics (IJCSI), ISSN 2231-5292, Vol.-II, Issue-1,2, during 27-28 January, 2012
7. “Color and Texture Features for Image Indexing and Retrieval” in International Journal of Electronics Communication and Computer Engineering -2012 (IJECE) ISBN 978-81-923249-0-6, Volume-3, during 27-28 January 2012.
8. “Implementation of FSK, BPSK & QPSK modulation techniques using FPGA” in an International Journal of Laboratory Experiments (LE-44) ISSN 0972-6055, Volume – 12, No-4, Dec-2012

Achievements

1. Received best teacher award for academic year 2013

Workshops

1. Two-week ISTE STTP WORKSHOP on “CMOS, Mixed Signal and Radio frequency VLSI Design” Conducted by Indian Institute of Technology Kharagpur from 30th January to 4th February 2017.
2. One-week FDP program on “RF ANTENNA DESIGN AND EVALUATION” Conducted by National Institute of Technology Warangal from 15-05-2017 to 20-05-2017 at Vardhaman College of Engineering, Hyderabad.
3. Two-week ISTE WORKSHOP on “Signals & Systems” Conducted by Indian Institute of Technology Kharagpur from 2nd to 12th January 2014.
4. Two-day National Level Workshop on “Telecommunication” Conducted by AJ Technology Resources held on 7th & 8th March 2013.
5. Two-week ISTE WORKSHOP on “Introduction to Research Methodologies” Conducted by Indian Institute of Technology Bombay from 25th June to 04th July 2012.
6. Workshop on “Signal, Image and Video Processing” organized by ECE Dept., at Malla Reddy College of Engineering and Technology during July 08-10, 2010.
7. Workshop on “Embedded Systems” organized by ECE dept., at CM Engineering College held on 29th August 2009.
8. Two-day workshop on “VLSI Semicustom Design with FPGA’s” organized by CM Engineering College on August 22-23, 2008.
9. One day workshop on “Methodology of Teaching for newly joined teachers” organized by Malla Reddy Engineering College on 24-09-2007.