

HARISH VEGOLAM

Personal Information



Designation: Assistant Professor

Qualification: M.Tech

Experience: 7 years

Specialization: Power Systems with emphasis on High Voltage

Date of Joining: 26th December, 2017

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Achievements / Publications / Workshops / Seminar / Guest Lectures

Publications:

1. Published “Design of Efficient ZVS Half-Bridge Series Inverter with suitable control Technique” Paper in International Journal.

Achievements:

1. Certified as an **ELITE** in **NPTEL** Online Certification Course (Funded by Ministry of HRD, Govt. of India) for **Power Systems Engineering** Course conducted by **IIT-Kharagpur**.

Workshops:

1. Participated in a one day Work shop on “RESEARCH WRITING” held on 10th January 2014.
2. Attended for FDP conducted on “TEACHER EFFECTIVENESS” on November, 2017 organized by TASK.
3. Attended Two Week FDP on “SIMULINK FOR SYSTEM & ALGORITHM MODELLING” during 24th May – 2nd June, 2018.