

Dr.R.Anand

Personal Information

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Designation: Associate Professor

Qualification: Ph.D

Experience: 11 Years

Specialization: Power Electronics

Date of Joining: 04.01.17

Phone Numbers: 9894102296

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Achievements/Publications/Workshops/Seminar/Guest Lectures

**Publications**

1. Mr.R.Anand ,Dr.IGnanambal, Mrs.N.Poornema, Design and Implementation of Fuzzy logic controller for a buck boost converter, International Review of Modelling and simulation, Vol : 7. No.2, 2014
2. Mr.R.Anand, Dr.IGnanambal, Mrs.N.Poornema, Implementation of Fuzzy logic controller for a buck boost converter combining KY and synchronous and buck converter for battery operated vehicles, Journal of Theoretical and applied information technology, Vol.67. No.1, 2014
3. N.Venkatarama, R.Anand, Dr.I.Gnanambal,Non Isolated High Gain Two Inductor Boost Converter for Solar PV Applications, Indian Journal of Communication Engineering and Systems (IJCES), Vol.2.No.2 2014.
4. R. Anand, I. Gnanambal, Modeling and Analysis of Cascade Multilevel DC-DC Boost Converter Topologies Based onH-bridge Switched Inductor, Research Journal of Applied Sciences, Engineering and Technology 9(3): 145-157, 2015ISSN: 2040-7459; e-ISSN: 2040-7467© Maxwell Scientific Organization, 2015
5. Anand R, Aravindh R, Kumaravel D, “ Elimination of transient overshoots in multi-level DC-DC converter by using Neural network controller”, International Journal for Research in Applied Science & Engineering Technology (IJRASET), ISSN 2321-9653, 2015.

6. Anand R, Aravindh R, “Output voltage ripple reduction in multilevel boost converter using neural networks” Output voltage ripple reduction in multilevel boost converter using neural networks” Volume 3, Special Issue-II, June 2015, ISSN: 2321-9653 2015
7. R.Anand, M.Kamatchi , “A Fifteen Level Cascaded H-Bridge Multilevel Inverter with Reduced Number of Switches” International Journal of Advanced Engineering Research and Science (IJAERS) ,Vol-2, Issue-11 , Nov- 2015 ISSN: 2349-6495,2015
8. Nathi.Venkanna, Dr.R.Anand, “ A Single-Phase Five-Level Photovoltaic (Pv) Inverter Topology For Grid-Connected PV Systems With A Novel Pulse Width-Modulated (Pwm) Control Scheme”, CMR Journal of Engineering and Technology, Vol.2 Issue.1 June, 2017,pp 37-53.
9. Dr.S.MuthuBalaji, Dr.R.Anand, “Design and Development of Multi agent system for Energy Management of Micro Grid”, CMR Journal of Engineering and Technology, Vol.2 Issue.1 June, 2017,pp 11-19.
10. Controlling the Power Loss in Radial Distribution Network by Optimally Placing UPQC through Reconfiguration Using Evolutionary Algorithms, International Conference on Cognitive Informatics & Soft Computing (CISC-2017)( Springer Conference)(Indexed by Scopus ) Vignana Bharati Institute of Technology, Hyderabad, 20-21 Dec 2017
11. A Novel Low Loss and Low Cost Solid State Circuit Breakers for Medium Voltage Applications, International Conference on Communications and Cyber Physical Engineering. (Springer Conference), CMR Institute of Technology, JANUARY 24-25, 2018
12. R.Anand, S.Muthu Balaji, “A Novel Cascaded Multilevel Inverter Topology with Minimal Switches”, International Journal of Engineering & Technology(UAE), Volume-7, Issue1.2, December 2017 ISSN : 2227-524X,p.p 205-210 ( Scopus Indexed Journal)
13. S.Muthu Balaji, R.Anand, P.Senthil Pandiyan “Performance enhancement analysis of isolated DC-DC Converter using fuzzy logic Controller”, International Journal of Engineering & Technology(UAE), Volume-7, Issue1.2, December 2017 ISSN : 2227-524X,p.p 11-19 ( Scopus Indexed Journal)

## **Achievements**

1. Have published 5 papers in Scopus indexed journals.

## **Workshops.**

1. Attended 2 days faculty development programme on Effective writing of technical papers organized by Department of Electrical and electronics engineering, National Institute of Technology, Trichy during 7th to 8th July 2012.
2. Attended a TEQIP-II sponsored one week faculty development programme on Advanced Digital Controller for Modern Special Machine Drives, Industrial Perspective conducted by the department of Electrical and Electronics Engineering, Government College of Engineering, Salem from 23rd to 29th October 2013.
3. Attended a two day Research and development programmed on MATLAB based training on Neural Networks in Engineering Applications conducted by Kongu Engineering College during 04th to 5th Feb 2013.
4. Attended a AICTE sponsored two week staff development programme on Power electronic application to Renewable Energy Generation organized by Department of Electrical and Electronics Engineering, Government college of Engineering, Salem from 29th April to 12th May 2013.
5. Attended a two day Faculty development program on Application of soft computing techniques in Electrical Systems conducted on 9th and 10th of December 2010 at Sri Eshwar College of Engineering, Coimbatore.
6. Participated in a two day National level workshop on Emerging trends in Power Engineering organized by the department of Electrical and Electronics engineering, SONA College of technology during 6th and 7th September 2012.
7. Participated in the NPTEL workshop conducted on 27.10.2012 organized by National Programme on Technology Enhanced learning (IIT Madras).
8. Participated in a National level Seminar on, “Application of Embedded systems in Renewable Energy, Power Electronics and Automotive systems” held at PSG college of technology, Coimbatore during 9th to 10th march 2012.
9. Participated in a two day workshop on Research issues in Power Converter and Power Systems organized by the Department of Electrical and Electronics engineering, Government College of Engineering, Salem on 16th and 17th of August 2012.
10. Participated in a two day workshop on Application of MATLAB/Simulink to Electrical Engineering organized by Power Electronics and Drives division, school of

Electrical engineering (SELECT) VIT University in association with MATHWORKS, Bangalore during 20-21 December 2010.

11. Participated in a One Day workshop on “ Intellectual Property Rights- What and How to apply”, organized by Mahendra College of Engineering, Salem on 12th March 2016.
12. Participated in a three day faculty development program conducted on Innovation & Development Program from 22 – 24 December 2016 at SONA College of Technology, Salem.
13. A seven days FDP at G. Narayanamma Institute of Technology & Science (for Women), Hyderabad on Inseminate Technical Institutes on OBE competence, R&D and IPR during 19th June -24th June 2017 organized by NIT Warangal / Electronics &ICT Academy