

Dr.S.Srinivasan

Personal Information



Designation: Associate Professor

Qualification: M.E., Ph.D

Academic Experience: 10 Years

Specialization: Harmonic Mitigation, Power Systems

Date of Joining: 24.06.2019

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Achievements/Publications/Workshops/Seminar/Guest Lectures

Publications

1. **S.Srinivasan, Dr.R.Ganesan, “Selective Harmonic Elimination Algorithm for a Boost H Bridge Inverter with Reduced Switch Configuration”, *Journal of Electrical Engineering (JEE)*, Vol.19, Issue. 1, No.11, April 2018, pp.89-94 (ISSN 1582-4594) (Scopus Indexed)**
2. **S.Srinivasan, Dr.R.Ganesan, Dr.A.Srinivasan “ Boost H-Bridge Inverter with Reduced Hardware Configuration for Harmonic Minimisation”, *Advances in Natural and Applied Sciences (ANAS)*, Vol. 11, No.7, May 2017, pp. 674-680 (AUC, Annexure II, Version 2017, Sl.No:559, ISSN : 1995-0772).**
3. **S.Srinivasan, Dr.R.Ganesan, “Flower Pollination Algorithm based Selective Harmonic Elimination PWM for an Eleven Level Inverter”, *Asian Journal of Research in Social Sciences and Humanities (AJRSSH)*, Vol. 6, Issue 6, Special Issue June 2016, pp. 69-84 (AUC, Annexure I, Version 2016, Sl.No:1108, ISSN : 2249-7315)**
4. **S.Srinivasan, Dr.R.Ganesan, M.Vijayalakshmi, “Power Quality Enhancement using Fuzzy Logic Controller based Shunt Hybrid Power Filter with Thyristor Controlled Reactor”, *International Journal of Applied Engineering Research*, Vol. 9 No.24 (2014) pp. 8344-8351 (Online ISSN 0973-9769) (Scopus Indexed).**
5. **S.Srinivasan, Dr.R.Ganesan, “Enriching Electric Power Quality by using Bee Algorithm in Shunt Active Filters”, *International Review on Modeling and Simulation (IREMOS)*, Vol. 7, No 4 (2014) (ISSN: 1974-9821) (Scopus Indexed).**

6.S.Srinivasan, K.Alagu Soundari, C.Kokila, L.Lakshmi Devi, “Shunt Active Power Filter for Distorted Power System Utility”, *International Journal of Emerging Trends in Engineering and Development (IJETED)*, Issue 3, Vol.2, March 2013, (ISSN 2249-6149) (Research Gate Indexed).

Conference Presentation International & National Level

- Presented a paper on **“Bat Algorithm based Selective Harmonic Elimination PWM for an Eleven Level Inverter”** in the **“International Conference on Modern Research & Computations in Electrical Technology” (ICMRCET-2019)** organized by Department of Electrical & Electronics Engineering, CMR College of Engineering & Technology, Hyderabad held during 9th and 10th July 2019.
- Presented a paper on **“Three Phase Shunt Active Power Filter with output LCL Filter”** in the **“International Conference on Advancements in Electrical and Electronics Engineering”** organized by Government College of Engineering, Salem held during 10th and 11th March 2014.
- Presented a paper on **“Implementation of a Current fed Full Bridge Boost DC-DC Converter with Zero Current Switching for High Voltage Applications”** in the **“National Conference on Recent Trends in Power Electronics and Drives”** organized by Christian College of Engineering & Technology, Oddanchatram held on 22nd March, 2011.
- Presented a paper on **“Comparison of Multiple Carrier PWM Techniques Applied for Multi- Level Shunt Active Filter”** in the **“National Conference on Recent Trends in Communication”** organized by Annai Mathammal Sheela Engineering College held during 11th and 12th of February 2010.
- Presented a paper on **“Power Filter using Conventional PWM based Inverters for Harmonic Reduction and Reactive Power Compensation”** in the **“International Conference on Intelligent Systems and Control”** organized by Karpagam College of Engineering, Coimbatore held during 6th and 7th of February 2009.

Achievements

- Reviewer of International Journal of Applied Electromagnetics and Mechanics (IJAEM); ISSN 1875-8800
- Reviewer of IEEE International Conference on Intelligent Techniques in Control, Optimization and Signal Processing
- Received “Certificate of Excellence Award” for producing meritorious results in various subjects.

Workshops / Short Term Courses

1. Participated in the Three day National Workshop on **“Assessment and Accreditation”** during 02.09.2015 – 04.09.2015 organized by Centre for Learning Technology, Kalasalingam University, Krishnankoil.
2. Participated in AICTE sponsored Faculty Development Programme on **“Harmonics and Power Quality – Contemporary Issues and Solutions”** during 22.04.2015 – 05.05.2015 organized by Department of EEE, Thiagarajar College of Engineering, Madurai.
3. Attended Faculty Development Program on **“Embedded System Application in Solar based Robotics”** held on 31st March 2015 organized by Department of EEE, Sethu Institute of Technology, Kariapatti.
4. Attended Two Days Workshop on **“Recent Trends in Solar Energy Technology”** during 18th & 19th February 2015 Organized by Sethu Institute of Technology, Kariapatti.
5. Participated in the 17th ISTE TN & P Section Annual Convention jointly organized by ISTE TN & P Section and National Engineering College, Kovilpatti during 07.11.2014 & 08.11.2014.
6. Attended National Seminar on **“Renewable Energy: Environmental solutions” [REED-2011]** during 28th & 29th Jan’2011 organized by Department of EEE, Saranathan college of Engineering, Tiruchirapalli.
7. Attended Faculty Development Program on **“Instrumentation & Automation”** during 5th May’2011 Organized by Department of EIE, Sethu Institute of Technology, Kariapatti.
8. Attended Workshop on **“Applications of Soft Computing Techniques in Electrical Engineering”** during October 11th & 12th 2011 Organized by Department of EEE, Bannari Amman Institute of Technology, Sathyamangalam.
9. Attended Workshop on **“Control System Design Using LABVIEW”** during April 3rd & 4th 2012 Organized by Department of EIE, K.L.N.College of Engineering, Madurai.
10. Attended a DST sponsored National Seminar on **“Challenges in remote sensing and GIS”** during March 25th & 26th 2013 Organized by Department of ECE, Sethu Institute of Technology, Kariapatti.
11. Attended a Faculty Improvement Program on **“Paradigms in Recent Research Methodology”** during May 6th & 7th 2013 Organized by Sethu Institute of Technology, Kariapatti.

12. Attended 3-day Faculty Development Program on **“Graphical System Design Using LABVIEW”**
13. organized by Government College of Technology, Coimbatore and National Instruments, Bangalore
14. Attended two day National level workshop on **“Virtual Instrumentation in Electrical Drives”** conducted by Karpagam College of Engineering in association with National Instruments (India) Limited and Siemens (India) Limited.
15. Attended one day workshop on **“Mobile Networking and Embedded Systems”** conducted by Government College of technology in association with Cueflux Technologies, Bangalore.