

CMR COLLEGE OF ENGINEERING & TECHNOLOGY

(UGC AUTONOMOUS)

B. Tech III Semester Supplementary Examinations February-2024

(B.Tech III Semester Supplementary Examinations February-2024 Course Name: OPERATING SYSTEMS					
	(Common for CSD, AID & AIM)					
<u>I</u>	Date: 05.02.2024 AN Time: 3 hours Max	.Marks: 70				
	(Note: Assume suitable data if necessary) PART-A Answer all TEN questions (Compulsory) Each question carries TWO marks.	10x2=20M				
1.	Define Operating System and list the objectives of Operating System.	0.14				
	What does the CPU do when there are no user programs to run?	2 M				
	List out the data fields associated with process control blocks.	2 M				
	What are the various states of a process and draw a diagram?	2 M				
	Outline the requirements that a critical section solution should satisfy.	2 M				
6.	What is Inter Process Communication? List out the calls involved in it.	2 M				
	Define Virtual Memory.	2 M				
	What are logical and physical addresses?	2 M				
	What is the role of file organization module in file system architecture?	2 M				
10.	List the attributes of a file.	2 M				
	PART-B	2 M				
<u>A</u>	Inswer the following. Food question country TEN M.	5x10=50M				
11.A).	Explain the purpose and importance of system calls in detail with examples. OR	10M				
11. B)	. Classify the types of system calls and explain any three types of it with its functions.	10M				
12. A)						
	OR					
12. B).	. Show how cooperating process can communicate each other via a message pas facility with an example.	sing 10M				
13. A).	y i man and a ma	5M				
	ii) Interpret Dining philosopher problem with an example.	5M				
	OR					
13. B).	How does deadlock can be avoided using banker's algorithm?	10M				
14. A).	Discuss the performance of demand paging. Illustrate the steps involved in handling page fault	ng a 10M				
	OR					
14. B).	Explain any two page replacement algorithms.	10M				
15. A).	Explain how file management is done in Linux.					
	OR	10M				
15. B).	i) Explain about the free space management.	5M				
	ii) Explain in detail about the file allocation methods in operating system.	5M				

PART-B

Answer the following. Each question carries TEN Marks.

1.

2.

3.

4.

5.

6.

7.

8.

9.

5x10=50M

Determine the positive root of $x \log_{10} x = 1.2$ lies between 0 and 1, by regula-falsi 10M method into four decimal places.

OR

Estimate the polynomial f(x) to fix the following data and the value of y at x = 5 by 10M using Lagrange's interpolation

x	4	6	10	8
у	1	3	8	16

Divide the range in to 6 equal parts, to find $\int_4^{5.2} \log_e x \ dx$ using Trapezoidal and Simpson's rule.

10M

OR

Use Fourth order Runge-Kutta method to find y(0.2), y(0.4) if $\frac{dy}{dx} = x^3 + y$, y(0) = 212. B). 10M (assume that h = 0.2).

(P.T.O..)

13. A). Identify the Laplace transform of "triangular wave" function f(t) defined by

10M

$$f(t) = \begin{cases} t & \text{if } 0 \le t \le a \\ 2a - t & \text{if } a \le t \le 2a \end{cases} \text{ and } f(t + 2a) = f(t) \text{ for all } t.$$

OR

13. B). Solve $y'' + 2y' - 3y = \sin t$, given y = y' = 0 at t = 0 using Laplace transform.

10M

14. A). A random variable X has the following probability function:

10M

$X = x_i$	0	1	2	3	4
P(X)	k	3 <i>k</i>	5 <i>k</i>	7 <i>k</i>	9 <i>k</i>
$=x_i$					

determine $\overline{(i)}$ the value of k and $\overline{(ii)}P[0 < X < 4]$ and $\overline{(iii)}P[X \ge 3]$.

OR

- 14. B). If X is normally distributed with mean 8 and variance 16, estimate the value of 10M (i) $P(5 \le X \le 10)$ (ii) $P(10 \le X \le 15)$ (iii) $P(X \ge 15)$.
- 15. A). Before an increase in excise duty on tea, 800 people out of a sample of 1000 were consumers of tea. After the increase in duty, 800 people were consumers of tea in a sample of 1200 persons. Test whether there is significant decrease in the consumptions of tea after the increase in duty.

OR

15. B). Two samples of size 9 and 8 gave the sums of squares of deviations from their respective means equal to 160 and 91 respectively. Can they be regarded as drawn from the same normal population?



CMR COLLEGE OF ENGINEERING & TECHNOLOGY (UGC AUTONOMOUS)

	B.Tech III Semester Supplementary Examinations February-2024 Course Name: COMPUTER ORGANIZATION & ARCHITECTURE	
· ·	(Common for CSE, IT, CSC, CSM, AID & AIM)	,
]	Date: 09.02.2024 AN Time: 3 hours Max.Mark	ks: 70
_	(Note: Assume suitable data if necessary)	
	PART-A Answer all TEN questions (Compulsory)	
	- ` · · · · · · · · · · · · · · · · · ·	=20M
1.	Define computer organization of a digital computer.	2 M
2.	Write Basic symbols for register transfer.	2 M
3.	Differentiate between restoring and non-restoring division algorithm.	2 M
4.	Represent the number $(+37.8)_{10}$ as a floating point binary number with normalized fraction mantissa 16 bits and exponent 8 bits.	2 M
5.	What is the need of I/O interface?	2 M
6.	Why does DMA have priority over the CPU when both request a memory transfer?	2 M
7.	Define pipelining.	2 M
8.	List the pipeline hazards.	2 M
9.	What is cache memory?	2 M
10.	What is meant by memory interleaving?	2 M
	PART-B	
_	Answer the following. Each question carries TEN Marks. 5x10=	=50M
11.A). i) Explain about Bus and Memory Transfer.	5M
	ii) Explain about Computer instructions.	5M
	OR	
11. E	3). i) Describe Instruction cycle in computer system.	5M
	ii) A Computer uses a memory unit with 256 K words of 32 bits each. A binary instruction code is stored in one word of memory. The instruction has four parts: an indirect bit, an operation code, a register code part to specify one of 64 registers, and an address part.	5M
	(a) How many bits are there in the operation code, the	
	register and enort, and the address nort?	
	register code part, and the address part?	
	(b) Draw the instruction word format and indicate the	
	(b) Draw the instruction word format and indicate the number of bits in each part.	
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12. A	 (b) Draw the instruction word format and indicate the number of bits in each part. (c) How many bits are there in the data and address inputs of the memory? a). i) Explain the process of floating point number multiplication with flow chart. 	5M
12. A	(b) Draw the instruction word format and indicate the number of bits in each part.(c) How many bits are there in the data and address inputs of the memory?	5M 5M
12. A	 (b) Draw the instruction word format and indicate the number of bits in each part. (c) How many bits are there in the data and address inputs of the memory? i) Explain the process of floating point number multiplication with flow chart. ii) Show the hardware for a 2 bit-by-2 bit array multiplier and explain its working. 	
	 (b) Draw the instruction word format and indicate the number of bits in each part. (c) How many bits are there in the data and address inputs of the memory? i) Explain the process of floating point number multiplication with flow chart. ii) Show the hardware for a 2 bit-by-2 bit array multiplier and explain its working. 	5M

13. A).	With a neat block diagram, explain the architecture of 8086 Microprocessor.	10M
	OR	
13. B).	i) What is interrupt? Why priority of interrupt is required? How it is restored?	5M
	ii) Give the comparison between programmed I/O and interrupt driven I/O.	5M
14. A).	Name the two pipeline organizations. Explain about the arithmetic pipeline with the help of an example.	10M
	OR	
14. B).	Explain instruction pipeline with neat timing diagram.	10M
15. A).	Draw and explain fully associative cache organization.	10M
	OR	10111
15. B).	Explain various page replacement algorithms.	10M



CMR COLLEGE OF ENGINEERING & TECHNOLOGY (UGC AUTONOMOUS)

Co	B.Tech III Semester Supplementary Examinations February-2 ourse Name: OBJECT ORIENTED PROGRAMMING THROUGH J.	
	(Common for CSC, CSM, AID & AIM)	
Da	te: 12.02.2024 AN Time: 3 hours	Max.Marks: 70
	(Note: Assume suitable data if necessary) PART-A Answer all TEN questions (Compulsory) Each question carries TWO marks.	10x2=20M
1. D	refine the basic characteristics of OOP.	2 M
	refine package? Write syntax to create a package.	2 M
	/hat is an Inner class?	2 M
	/hat are the benefits of Exception Handling?	2 M
	ist out the states of Thread.	2 M
	/hat is the use of PrintStream class?	2 M
	/hat is JDBC?	2 M
	/hat is the use of enum keyword.	2 M
	refine the scala.	2 M
	ifferentiate between the Event sources and Event Listeners.	2 M
		2/ 1/1
	PART-B	
<u>An</u>	swer the following. Each question carries TEN Marks.	5x10=50M
11.A).	William I OF III I I I I I I I I I I I I I I I	
	What is polymorphism? Explain how polymorphism is implemented in java vexample.	with suitable 10M
· · · · · · · · · · · · · · · · · ·		with suitable 10M
11. B).	example.	with suitable 10M
ŕ	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks.	
11. B). 12. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR	10M 10M
11. B).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples.	10M
11. B). 12. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR	10M 10M
11. B). 12. A). 12. B). 13. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR	10M 10M 10M
11. B). 12. A). 12. B).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java?	10M 10M 10M
11. B). 12. A). 12. B). 13. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR	10M 10M 10M
11. B). 12. A). 12. B). 13. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java?	10M 10M 10M 10M 5M 5M
11. B). 12. A). 12. B). 13. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java? ii) Develop a java program to read input from the user using Scanner class?	10M 10M 10M 10M 5M
11. B). 12. A). 12. B). 13. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java? ii) Develop a java program to read input from the user using Scanner class? i) Outline significance of collection interface.	10M 10M 10M 10M 5M 5M
11. B). 12. A). 12. B). 13. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java? ii) Develop a java program to read input from the user using Scanner class? i) Outline significance of collection interface. ii) List and explain the various methods defined in collection interface.	10M 10M 10M 10M 5M 5M
11. B). 12. A). 12. B). 13. A). 13. B).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java? ii) Develop a java program to read input from the user using Scanner class? i) Outline significance of collection interface. ii) List and explain the various methods defined in collection interface. OR Explain the steps involved in building JDBC application.	10M 10M 10M 10M 5M 5M 5M 5M
11. B). 12. A). 12. B). 13. A). 14. A).	OR Define Interface? Illustrate the implementation of interface with example. Define Exception? Create a java program using try, catch and finally blocks. OR Distinguish static Inner Class and Local Inner class with examples. Explain the different ways of creating Threads with example. OR i) Classify the stream classes in java? ii) Develop a java program to read input from the user using Scanner class? i) Outline significance of collection interface. ii) List and explain the various methods defined in collection interface. OR	10M 10M 10M 10M 5M 5M 5M



CMR COLLEGE OF ENGINEERING & TECHNOLOGY (UGC AUTONOMOUS)

B.Tech III Semester Supplementary Examinations February-2024

Course Name: BASIC ELECTRICAL ENGINEERING

(Common for CSM & AIM)

Date: 14.02.2024 AN Time: 3 hours

Max.Marks: 70

(Note: Assume suitable data if necessary)

PART-A

Answer all TEN questions (Compulsory)

Each question carries TWO marks.

10x2 = 20M

- 1. Define Ohm's Law and its limitations.
- Explain Kirchhoff's Current Law. 2.
- Define Form factor and peak factor of an alternating quantity. 3.
- 2 M An alternating voltage is given by $v = 300 \sin(377t+30^{\circ})$ V find i) maximum voltage ii) 4. 2 M Frequency.
- 5. Classify the Generators based on Excitation.
- 6. List out the Essential parts of DC machine.
- 7. List the properties of an Ideal Transformer.
- The primary winding of transformer has 400 turns and secondary winding has 50 turns .If the 8. primary voltage is 120V(RMS) find the secondary voltage.
- 9. Define Slip.
- 10. Classify the Single phase Induction motors.

2 M

2 M

2 M

2 M 2 M

2 M

2 M 2 M

PART-B

Answer the following. Each question carries TEN Marks.

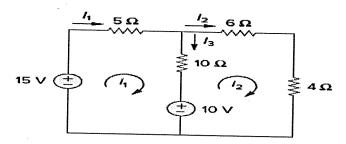
5x10=50M

State and Explain Thevenin's theorem with an example. 11.A).

10M

11. B). For the circuit shown in fig. solve for the branch currents $i_1, i_2 & i_3$ using Mesh Analysis.

10M



12. A). A 220 V, 1-φ, 50 Hz AC supply is applied across series connection of $R = 10 \Omega$. L =10M 0.1 H. Determine impedance, current, voltage across R, voltage across L, power factor, active power, reactive power and apparent power. Also draw the phasor diagram.

Develop the relation between phase and line values of a 3-phase balanced star connected 12. B). 10M and delta connected system.

(P.T.O..)

13. A).	Explain the construction and principle and operation of DC generator.	10M
	OR	
13. B).	A 10 KW,250 V D.C shunt machine has an armature resistance as 0.1 Ω and field resistance of 100 Ω . Determine the armature power developed when i) it runs as a generator delivering 10 KW, ii) it runs as a motor taking an input of 10 KW.	10M
14. A).	Draw and explain the phasor diagram of a transformer, on no load.	10M
	OR	
14. B).	A 25KVA ,2200/220 V,50Hz transformer, has the following test data:	10M
	OC test:220 V, 4 A,150 W	
	SC test :90 V, 10 A,350 W	
	Determine:	
	i) the efficiency at full load, unity power factor	
	ii) the efficiency at half full load, 0.8 p.f. lag	
	iii) the efficiency at half full load, 0.8 p.f.lead.	
	iv) find the regulation at full load, unity p.f	
15. A).	Explain the production of rotating magnetic field in a three phase induction motor.	10M
	OR	
15. B).	Explain briefly about single phase induction motors with the help of neat circuit diagrams.	10M
