

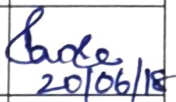
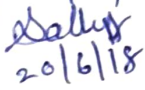
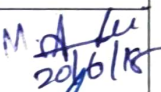
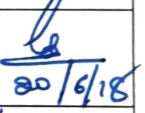
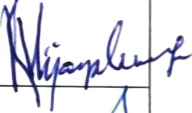
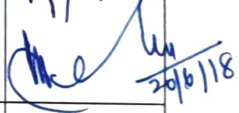
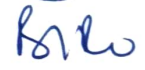
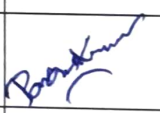

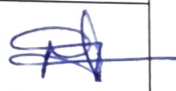


CMRCET/ECE/BoS/2018/06

Date: 20th June 2018

Department of Electronics & Communication Engineering

BOS MEETING CONDUCTED ON 20TH JUNE 2018

Members Present:

S. No	Position	Composition	Name	Signature
1	Chairman	Professor & HOD, Department of ECE, CMRCET	Mr. E. N. V. Purna Chandra Rao	 20/06/18
2	Member	Expert nominated by Vice Chancellor/ JNTUH	Dr. T. Satya Savithri, Professor & Head, Department of ECE, JNTUH College of Engineering, Hyderabad	 20/6/18
3	Member	Expert nominated by academic council	Dr. M. AshaRani, Professor, Department of ECE, JNTUH College of Engineering, Hyderabad	 20/6/18
4	Member	Expert nominated by academic council (Industry Rep)	Dr.M.Mallikarjun, Scientist- G, RCI, DRDO, Hyderabad	 20/6/18
5	Member	Expert nominated by academic council (Industry Rep)	Mr. Vijay Kumar Gupta, CEO, Kwality Photonics P Ltd, Hyderabad.	
6	Member	Professor in ECE & Dean (Planning & Development), CMRCET	Dr. M. Sudhakar	 20/6/18
7	Member	Professor in ECE & Dean (Academics), CMRCET	Dr. B. Lokeswara Rao	
8	Member	Alumnus Nominated by Principal	Mr. Pavan Kumar V, Senior consultant, Capgemini India Pvt. Ltd.	
9	Member	Alumnus Nominated by Principal	Mr. Pramod Sravan, Senior Associate, FACTSET.	
10	Member	Senior Faculty Member of ECE	Dr. M. Nagaraju Naik	
11	Member	Senior Faculty Member of ECE	Dr. Vandhana Khare	
12	Member	Senior Faculty Member of ECE	Mr. P. Raveendra Babu	



Prof. E.N.V. Purna Chandra Rao
Head of the Department
Department of ECE
CMR College of Engg. & Technology
Kandlakoya, Medchal, Hyderabad-501401.



CMR COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

NAAC Accredited Institution with 'A' Grade
Kandlakoya, Medchal Road, Hyderabad-501401

CMRCET/ECE/BoS/2018/07

Date: 24th June, 2018

Minutes of Meeting – Board of Studies- June 2018

Board of Studies meeting was held on 20th June 2018 to frame the curriculum for B. Tech (ECE) and M. Tech (Embedded Systems).

The following dignitaries were attended the meeting

1. Mr. E. N. V. Purna Chandra Rao, Professor & HOD- ECE
2. Dr. T. Satya Savithri, Professor & HOD - ECE, JNTUH
3. Dr. M. Asha Rani, Professor – ECE, JNTUH
4. Dr. M. Mallikarjun, Scientist, DRDO, Hyderabad.
5. Mr. Vijay Kumar Gupta, CEO, Kwalify Photonics P Ltd, Hyderabad.
6. Dr. M. Sudhakar, Professor in ECE and Dean (Planning & Development)
7. Dr. B. Lokeswara Rao, Professor in ECE and Dean (Academics)
8. Mr. Pavan Kumar V, Senior consultant, Capgemini India Pvt. Ltd.
9. Mr. Pramod Sravan, Senior Associate, FACTSET.
10. Dr. M. Nagraju Naik, Professor, ECE
11. Dr. Vandana Khare, Professor, ECE
12. Mr. P. Raveendra Babu, Associate Professor, ECE

ITEM I: Course structure for B.Tech (ECE) and detailed syllabus for B. Tech (ECE)

1. Electrical Engineering lab is placed in III semester instead of IV semester.
2. Basic simulation Lab is shifted to IV semester from III semester.
3. "Probability Theory & Random Processes" subject name changed as "Probability & Stochastic Processes".
4. Analog Communications & Digital communications were thought separately in IVth and Vth semester separately, but now combined as "Analog & Digital Communications" subject was placed in the IV semester.
5. As per members suggestion the course outcomes are modified.

Resolution: The members approved the course structure and syllabus of B.Tech(Electronics & Communication Engineering) which is enclosed.

ITEM-II: Course structure and detailed syllabus of M.Tech (Embedded Systems)

1. "Embedded Programming" subject was included in I semester.
2. "Embedded Programming Lab" and "Scripting Language Lab" were newly introduced in I semester.
3. "Programmable System on Chip Lab" was included in II semester.
4. "System Design with Embedded Linux" and "Embedded Networking" were newly introduced as professional electives in I semester.
5. "Analog and Digital CMOS VLSI design", "Communication Buses & Interfaces" were newly introduced as professional electives in II semester.
6. "Research Methodology and IPR" was included in I semester.
7. "English for Research Paper Writing" was included as audit course in I semester, "Value Education" was included as audit course in II semester.
8. Mini project with seminar included in II semester.

9. New subjects introduced in III semester as professional electives are Design of Fault Tolerant systems, CMOS Mixed Signal Design and Hardware Software Co-Design where as in open elective other departments like CSE,EEE and CIVIL subjects included.

Resolution: The members approved the course structure and syllabus of M.Tech (Embedded Systems) which is enclosed.

The meeting concluded with the chairman thanking all members for sparing their valuable time and suggestions.



HOD- ECE

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